**Design Project 1: Design and Implementation of Combinational Circuits**

**Validation Sheet**

*The validation sheet is provided as an example of how the GTA will test your implementation.*

*You do not have to go the CEL to have your implementation validated in person.*

Name:

The logic switches on the DE0 Nano Board represent inputs to a seven-segment LED display driver circuit. Apply all ten input combinations and record your observations in the table below by ***shading in the lit segments for each input combination****.*

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Input Value  SW[3:0] | 0000 | 0011 | 0010 | 0100 | 0111 |
| 7-segment LED Display |  |  |  |  |  |
| Decimal Digit | 0 | 1 | 2 | 3 | 4 |
| Input Value  SW[3:0] | 1000 | 1011 | 1101 | 1100 | 1111 |
| 7-segment LED Display |  |  |  |  |  |
| Decimal Digit | 5 | 6 | 7 | 8 | 9 |

**Comments:** (Describe discrepancies or any questionable behaviors in the circuit or validation process.)

Validated by:

GTA: **Print** your name. GTA: **Sign** your name**.**

Date and Time: